

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 53(b) Divisional Application of **10/100,011**:

YONEDA, Yoshiyuki et al.

Group Art Unit: **2827 (expected)**

Serial No.: (New)

Examiner: **David A. ZAMEKE (expected)**

Filed: **August 29, 2003**

P.T.O. Confirmation No.: 4633

For. **MANUFACTURING METHOD OF A SEMICONDUCTOR DEVICE
INCORPORATING A PASSIVE ELEMENT AND A REDISTRIBUTION BOARD**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 29, 2003

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. **10/100,011**.

The Director is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP



Mel R. Quintos

Attorney for Applicants

Reg. No. 31,898

MRQ:lrj

Atty. Docket No. **020330A**
1725 K Street, N.W; Suite 1000
Washington, D.C. 20006
(202) 659-2930



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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 020330A	Serial No. New Divisional Appln.
	Applicant(s): YONEDA, Yoshiyuki et al.	
	Filing Date: August 29, 2003	Group Art Unit: 2827 (expected)

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					
_____	AE					
_____	AF					
_____	AG					
_____	AH					

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)
_____ AI	2000-306771	11/02/2000	Japan	Abstract
_____ AJ	2001-274036	10/05/2001	Japan	Abstract
_____ AK				

OTHER DOCUMENTS

_____	AL					
_____	AM					
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